



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for III B.Tech I semester (R13/R10) Supplementary Examinations Nov-2019

College name: ST.MARY'S WOMEN'S ENGG COLLEGE, BUDAMPADU, GUNTUR:ND

| Htno | Subcode | Subname | Internal | External | CREDITS |
|------------|---------|---|----------|----------|---------|
| 10ND1A0201 | R31021 | COMPLEX VARIABLES AND STATISTICAL METHO | 13 | 17 | 0 |
| 10ND1A0201 | R31023 | POWER SYSTEMS-II | 13 | 28 | 4 |
| 11AR1A0503 | R31056 | OPERATIONG SYSTEMS | 15 | -1 | 0 |
| 11ND1A0521 | R31054 | COMPUTER GRAPHICS | 16 | 7 | 0 |
| 11ND1A0521 | R31055 | MICRO PROCESSORS AND MULTICORE SYSTEMS | 15 | 13 | 0 |
| 13ND1A0421 | RT31041 | PULSE & DIGITAL CIRCUITS | 21 | 36 | 3 |
| 13ND1A0427 | RT31043 | CONTROL SYSTEMS | 21 | 9 | 0 |
| 13ND1A0427 | RT31045 | ANTENNAS AND WAVE PROPAGATION | 20 | 18 | 0 |
| 13ND1A0453 | RT31042 | LINEAR INTEGRATED CIRCUIT APPLICATIONS | 8 | 29 | 0 |
| 13ND1A0462 | RT31041 | PULSE & DIGITAL CIRCUITS | 18 | 29 | 3 |
| 13ND1A0477 | RT31042 | LINEAR INTEGRATED CIRCUIT APPLICATIONS | 13 | 7 | 0 |
| 13ND1A04A1 | RT31044 | DIGITAL SYSTEM DESIGN & DICA | 21 | 11 | 0 |
| 13ND1A04A5 | RT31041 | PULSE & DIGITAL CIRCUITS | 18 | -1 | 0 |
| 13ND1A04A5 | RT31043 | CONTROL SYSTEMS | 22 | 15 | 0 |
| 13ND1A04A5 | RT31044 | DIGITAL SYSTEM DESIGN & DICA | 15 | 13 | 0 |
| 13ND1A04C3 | RT31041 | PULSE & DIGITAL CIRCUITS | 19 | 34 | 3 |
| 13ND1A04D4 | RT31041 | PULSE & DIGITAL CIRCUITS | 15 | -1 | 0 |
| 13ND1A04D4 | RT31043 | CONTROL SYSTEMS | 19 | 13 | 0 |
| 13ND1A04F2 | RT31041 | PULSE & DIGITAL CIRCUITS | 22 | 36 | 3 |
| 13ND1A04H0 | RT31041 | PULSE & DIGITAL CIRCUITS | 20 | 29 | 3 |
| 13ND1A04H8 | RT31041 | PULSE & DIGITAL CIRCUITS | 17 | 30 | 3 |
| 13ND1A0533 | RT31051 | COMPILER DESIGN | 21 | 32 | 3 |
| 13ND1A0567 | RT31052 | DATA COMMUNICATION | 21 | 17 | 0 |
| 13ND1A0567 | RT31053 | PRINCIPLES OF PROGRAMMING LANGUAGES | 21 | 17 | 0 |
| 13ND1A0567 | RT31054 | DATABASE MANAGEMENT SYSTEMS | 18 | 36 | 3 |
| 13ND1A0567 | RT31055 | OPERATING SYSTEMS | 20 | 6 | 0 |
| 14ND1A0415 | RT31041 | PULSE & DIGITAL CIRCUITS | 15 | 18 | 0 |
| 14ND1A0415 | RT31043 | CONTROL SYSTEMS | 14 | -1 | 0 |
| 14ND1A0415 | RT31045 | ANTENNAS AND WAVE PROPAGATION | 9 | -1 | 0 |
| 14ND1A0469 | RT31045 | ANTENNAS AND WAVE PROPAGATION | 22 | 16 | 0 |
| 14ND1A0474 | RT31044 | DIGITAL SYSTEM DESIGN & DICA | 18 | 9 | 0 |
| 14ND1A0476 | RT31045 | ANTENNAS AND WAVE PROPAGATION | 19 | 15 | 0 |
| 14ND1A0483 | RT31041 | PULSE & DIGITAL CIRCUITS | 19 | 28 | 3 |
| 14ND1A0483 | RT31043 | CONTROL SYSTEMS | 20 | -1 | 0 |
| 14ND1A0483 | RT31044 | DIGITAL SYSTEM DESIGN & DICA | 23 | 13 | 0 |
| 14ND1A0489 | RT31041 | PULSE & DIGITAL CIRCUITS | 24 | 23 | 0 |
| 14ND1A04B3 | RT31041 | PULSE & DIGITAL CIRCUITS | 20 | 31 | 3 |
| 14ND1A04B5 | RT31045 | ANTENNAS AND WAVE PROPAGATION | 20 | 29 | 3 |
| 14ND1A04D2 | RT31042 | LINEAR INTEGRATED CIRCUIT APPLICATIONS | 20 | 11 | 0 |
| 14ND1A04E3 | RT31045 | ANTENNAS AND WAVE PROPAGATION | 24 | 32 | 3 |
| 14ND1A04F5 | RT31041 | PULSE & DIGITAL CIRCUITS | 23 | -1 | 0 |
| 14ND1A04F5 | RT31042 | LINEAR INTEGRATED CIRCUIT APPLICATIONS | 19 | 9 | 0 |
| 14ND1A04F8 | RT31045 | ANTENNAS AND WAVE PROPAGATION | 22 | 30 | 3 |
| 14ND1A05B3 | RT31053 | PRINCIPLES OF PROGRAMMING LANGUAGES | 22 | 19 | 0 |
| 15ND1A0423 | RT31042 | LINEAR INTEGRATED CIRCUIT APPLICATIONS | 29 | 17 | 0 |

| Htno | Subcode | Subname | Internal | External | CREDITS |
|------------|---------|--|----------|----------|---------|
| 15ND1A0429 | RT31016 | IPR & PATENTS | 21 | 28 | 2 |
| 15ND1A0433 | RT31042 | LINEAR INTEGRATED CIRCUIT APPLICATIONS | 25 | 11 | 0 |
| 15ND1A0452 | RT31041 | PULSE & DIGITAL CIRCUITS | 24 | 28 | 3 |
| 15ND1A0553 | RT31051 | COMPILER DESIGN | 21 | 16 | 0 |
| 15ND1A0553 | RT31053 | PRINCIPLES OF PROGRAMMING LANGUAGES | 12 | 13 | 0 |
| 15ND1A0556 | RT31051 | COMPILER DESIGN | 21 | 17 | 0 |
| 15ND1A0556 | RT31052 | DATA COMMUNICATION | 11 | 15 | 0 |
| 15ND1A0556 | RT31053 | PRINCIPLES OF PROGRAMMING LANGUAGES | 10 | 11 | 0 |
| 15ND1A0556 | RT31054 | DATABASE MANAGEMENT SYSTEMS | 3 | 23 | 0 |
| 15ND1A0556 | RT31055 | OPERATING SYSTEMS | 13 | 0 | 0 |
| 15ND1A0570 | RT31054 | DATABASE MANAGEMENT SYSTEMS | 24 | 40 | 3 |
| 15ND1A0584 | RT31054 | DATABASE MANAGEMENT SYSTEMS | 21 | 24 | 3 |
| 15ND1A05E5 | RT31051 | COMPILER DESIGN | 21 | 29 | 3 |
| 15ND1A05E5 | RT31055 | OPERATING SYSTEMS | 19 | 12 | 0 |
| 15ND1A05F9 | RT31052 | DATA COMMUNICATION | 19 | 11 | 0 |
| 15ND1A05F9 | RT31054 | DATABASE MANAGEMENT SYSTEMS | 16 | 36 | 3 |
| 16ND5A0402 | RT31041 | PULSE & DIGITAL CIRCUITS | 18 | 31 | 3 |

****Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 20-01-2020]**

**** Note:****

* -1 in the filed of externals indicates student is absent for the respective subject.

* -2 in the filed of externals indicates student result Withheld for the respective subject.

* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.



Date:10.01.2020

Controller of Examinations