



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for II B.Tech I semester (R16) Regular/Supplementary Examinations Nov-2019

College name: ST.MARY'S WOMEN'S ENGG COLLEGE, BUDAMPADU, GUNTUR:ND

Htno	Subcode	Subname	Grade	Credits
16BJ1A0424	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16BJ1A0424	R1621044	NETWORK ANALYSIS	F	0
16ND1A0410	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
16ND1A0410	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16ND1A0410	R1621043	SIGNALS AND SYSTEMS	F	0
16ND1A0410	R1621044	NETWORK ANALYSIS	F	0
16ND1A0412	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
16ND1A0412	R1621044	NETWORK ANALYSIS	F	0
16ND1A0419	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
16ND1A0419	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
16ND1A0419	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16ND1A0419	R1621043	SIGNALS AND SYSTEMS	F	0
16ND1A0419	R1621044	NETWORK ANALYSIS	F	0
16ND1A0420	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16ND1A0434	R1621043	SIGNALS AND SYSTEMS	D	3
16ND1A0437	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16ND1A0437	R1621043	SIGNALS AND SYSTEMS	F	0
16ND1A0437	R1621044	NETWORK ANALYSIS	D	3
16ND1A0438	R1621044	NETWORK ANALYSIS	F	0
16ND1A0449	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
16ND1A0449	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16ND1A0454	R1621044	NETWORK ANALYSIS	F	0
16ND1A0457	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16ND1A0457	R1621044	NETWORK ANALYSIS	F	0
16ND1A0457	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
16ND1A0465	R1621044	NETWORK ANALYSIS	C	3
16ND1A0469	R1621044	NETWORK ANALYSIS	F	0
16ND1A0470	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
16ND1A0470	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16ND1A0470	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
16ND1A0480	R1621043	SIGNALS AND SYSTEMS	F	0
16ND1A0480	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
16ND1A0487	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
16ND1A0487	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16ND1A0487	R1621043	SIGNALS AND SYSTEMS	D	3
16ND1A0501	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
16ND1A0503	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0503	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0503	R1621054	PYTHON PROGRAMMING	F	0
16ND1A0505	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0507	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0507	R1621054	PYTHON PROGRAMMING	F	0
16ND1A0507	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A0513	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0513	R1621055	DATA STRUCTURES THROUGH C++	D	3

Htno	Subcode	Subname	Grade	Credits
16ND1A0515	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16ND1A0515	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A0516	R1621051	STATISTICS WITH R PROGRAMMING	F	0
16ND1A0516	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0522	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0523	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A0527	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0527	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0527	R1621054	PYTHON PROGRAMMING	D	3
16ND1A0527	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A0531	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0531	R1621054	PYTHON PROGRAMMING	F	0
16ND1A0532	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0532	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0535	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16ND1A0536	R1621051	STATISTICS WITH R PROGRAMMING	D	3
16ND1A0541	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0541	R1621053	DIGITAL LOGIC DESIGN	C	3
16ND1A0542	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0544	R1621055	DATA STRUCTURES THROUGH C++	C	3
16ND1A0545	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0546	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0546	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A0547	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0547	R1621054	PYTHON PROGRAMMING	F	0
16ND1A0550	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0550	R1621054	PYTHON PROGRAMMING	C	3
16ND1A0555	R1621051	STATISTICS WITH R PROGRAMMING	F	0
16ND1A0555	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0555	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0555	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A0559	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16ND1A0559	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0563	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0567	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0567	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0567	R1621054	PYTHON PROGRAMMING	D	3
16ND1A0567	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A0570	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0571	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16ND1A0571	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A0572	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
16ND1A0578	R1621054	PYTHON PROGRAMMING	F	0
16ND1A0584	R1621054	PYTHON PROGRAMMING	F	0
16ND1A0587	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0589	R1621054	PYTHON PROGRAMMING	C	3
16ND1A0589	R1621055	DATA STRUCTURES THROUGH C++	D	3
16ND1A0590	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
16ND1A0591	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A0596	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
16ND1A0596	R1621053	DIGITAL LOGIC DESIGN	F	0

Htno	Subcode	Subname	Grade	Credits
16ND1A05A0	R1621051	STATISTICS WITH R PROGRAMMING	F	0
16ND1A05A0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05A3	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05A5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05A7	R1621053	DIGITAL LOGIC DESIGN	D	3
16ND1A05A7	R1621054	PYTHON PROGRAMMING	D	3
16ND1A05A9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16ND1A05A9	R1621053	DIGITAL LOGIC DESIGN	C	3
16ND1A05B4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05B4	R1621054	PYTHON PROGRAMMING	F	0
16ND1A05B8	R1621051	STATISTICS WITH R PROGRAMMING	F	0
16ND1A05B8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05B9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05C0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05C1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05C1	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05C2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05C2	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05C3	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A05C8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05C8	R1621053	DIGITAL LOGIC DESIGN	C	3
16ND1A05C9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05C9	R1621053	DIGITAL LOGIC DESIGN	D	3
16ND1A05D2	R1621051	STATISTICS WITH R PROGRAMMING	D	3
16ND1A05D2	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05D2	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A05D3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05D3	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05D4	R1621053	DIGITAL LOGIC DESIGN	C	3
16ND1A05D5	R1621051	STATISTICS WITH R PROGRAMMING	F	0
16ND1A05D8	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05D8	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A05E0	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05E0	R1621055	DATA STRUCTURES THROUGH C++	C	3
16ND1A05E1	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05E5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05E6	R1621054	PYTHON PROGRAMMING	F	0
16ND1A05E6	R1621055	DATA STRUCTURES THROUGH C++	F	0
16ND1A05E7	R1621054	PYTHON PROGRAMMING	C	3
16ND1A05E7	R1621055	DATA STRUCTURES THROUGH C++	D	3
16ND1A05E9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05E9	R1621053	DIGITAL LOGIC DESIGN	D	3
16ND1A05F1	R1621053	DIGITAL LOGIC DESIGN	D	3
16ND1A05F5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05F5	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05F5	R1621054	PYTHON PROGRAMMING	C	3
16ND1A05G2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05G2	R1621053	DIGITAL LOGIC DESIGN	F	0
16ND1A05H3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16ND1A05H3	R1621053	DIGITAL LOGIC DESIGN	D	3
16ND1A05H4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3

Htno	Subcode	Subname	Grade	Credits
16ND1A05H4	R1621053	DIGITAL LOGIC DESIGN	D	3
17ND1A0402	R1621044	NETWORK ANALYSIS	B	3
17ND1A0404	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
17ND1A0405	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17ND1A0405	R1621044	NETWORK ANALYSIS	C	3
17ND1A0405	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0406	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A0406	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0407	R1621044	NETWORK ANALYSIS	F	0
17ND1A0409	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A0409	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0410	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
17ND1A0410	R1621044	NETWORK ANALYSIS	F	0
17ND1A0412	R1621043	SIGNALS AND SYSTEMS	F	0
17ND1A0412	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0413	R1621043	SIGNALS AND SYSTEMS	F	0
17ND1A0413	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0414	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
17ND1A0415	R1621044	NETWORK ANALYSIS	C	3
17ND1A0417	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
17ND1A0417	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A0417	R1621043	SIGNALS AND SYSTEMS	F	0
17ND1A0417	R1621044	NETWORK ANALYSIS	D	3
17ND1A0417	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0418	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0421	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0425	R1621044	NETWORK ANALYSIS	F	0
17ND1A0425	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
17ND1A0428	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
17ND1A0432	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0433	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17ND1A0434	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
17ND1A0435	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
17ND1A0437	R1621044	NETWORK ANALYSIS	A	3
17ND1A0437	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0438	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0440	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17ND1A0440	R1621043	SIGNALS AND SYSTEMS	C	3
17ND1A0443	R1621044	NETWORK ANALYSIS	D	3
17ND1A0444	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
17ND1A0448	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
17ND1A0448	R1621043	SIGNALS AND SYSTEMS	B	3
17ND1A0448	R1621044	NETWORK ANALYSIS	C	3
17ND1A0450	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A0450	R1621044	NETWORK ANALYSIS	F	0
17ND1A0450	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0452	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
17ND1A0452	R1621043	SIGNALS AND SYSTEMS	F	0
17ND1A0455	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0456	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17ND1A0456	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0

Htno	Subcode	Subname	Grade	Credits
17ND1A0456	R1621044	NETWORK ANALYSIS	F	0
17ND1A0457	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
17ND1A0460	R1621044	NETWORK ANALYSIS	F	0
17ND1A0464	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A0464	R1621044	NETWORK ANALYSIS	F	0
17ND1A0464	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0465	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A0465	R1621044	NETWORK ANALYSIS	F	0
17ND1A0467	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17ND1A0467	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0473	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A0473	R1621043	SIGNALS AND SYSTEMS	F	0
17ND1A0473	R1621044	NETWORK ANALYSIS	F	0
17ND1A0473	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0475	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0480	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
17ND1A0480	R1621044	NETWORK ANALYSIS	F	0
17ND1A0480	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A0481	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A0481	R1621044	NETWORK ANALYSIS	D	3
17ND1A0484	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
17ND1A0492	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
17ND1A0492	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17ND1A0492	R1621043	SIGNALS AND SYSTEMS	F	0
17ND1A0492	R1621044	NETWORK ANALYSIS	F	0
17ND1A0492	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A04A5	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17ND1A04A5	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A04A5	R1621044	NETWORK ANALYSIS	F	0
17ND1A04A5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND1A04A7	R1621044	NETWORK ANALYSIS	B	3
17ND1A04A8	R1621044	NETWORK ANALYSIS	C	3
17ND1A04B3	R1621044	NETWORK ANALYSIS	F	0
17ND1A04B8	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND1A04B9	R1621044	NETWORK ANALYSIS	D	3
17ND1A04C1	R1621044	NETWORK ANALYSIS	D	3
17ND1A0502	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
17ND1A0502	R1621053	DIGITAL LOGIC DESIGN	C	3
17ND1A0502	R1621054	PYTHON PROGRAMMING	D	3
17ND1A0507	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0511	R1621051	STATISTICS WITH R PROGRAMMING	D	3
17ND1A0511	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0511	R1621054	PYTHON PROGRAMMING	D	3
17ND1A0511	R1621055	DATA STRUCTURES THROUGH C++	F	0
17ND1A0514	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A0514	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0518	R1621054	PYTHON PROGRAMMING	F	0
17ND1A0521	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A0521	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A0521	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0521	R1621054	PYTHON PROGRAMMING	F	0

Htno	Subcode	Subname	Grade	Credits
17ND1A0521	R1621055	DATA STRUCTURES THROUGH C++	D	3
17ND1A0521	R1621056	COMPUTER GRAPHICS	F	0
17ND1A0526	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A0528	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0531	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0533	R1621055	DATA STRUCTURES THROUGH C++	F	0
17ND1A0534	R1621051	STATISTICS WITH R PROGRAMMING	C	3
17ND1A0535	R1621053	DIGITAL LOGIC DESIGN	C	3
17ND1A0536	R1621053	DIGITAL LOGIC DESIGN	D	3
17ND1A0539	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
17ND1A0539	R1621058	PYTHON PROGRAMMING LAB	S	2
17ND1A0540	R1621053	DIGITAL LOGIC DESIGN	D	3
17ND1A0542	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0542	R1621055	DATA STRUCTURES THROUGH C++	F	0
17ND1A0543	R1621053	DIGITAL LOGIC DESIGN	C	3
17ND1A0543	R1621054	PYTHON PROGRAMMING	C	3
17ND1A0545	R1621053	DIGITAL LOGIC DESIGN	C	3
17ND1A0545	R1621054	PYTHON PROGRAMMING	B	3
17ND1A0546	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A0546	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A0546	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0546	R1621056	COMPUTER GRAPHICS	F	0
17ND1A0548	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A0548	R1621054	PYTHON PROGRAMMING	F	0
17ND1A0548	R1621056	COMPUTER GRAPHICS	F	0
17ND1A0549	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A0549	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A0549	R1621054	PYTHON PROGRAMMING	F	0
17ND1A0550	R1621055	DATA STRUCTURES THROUGH C++	C	3
17ND1A0552	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0552	R1621054	PYTHON PROGRAMMING	D	3
17ND1A0555	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A0555	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0555	R1621054	PYTHON PROGRAMMING	F	0
17ND1A0555	R1621055	DATA STRUCTURES THROUGH C++	D	3
17ND1A0555	R1621056	COMPUTER GRAPHICS	F	0
17ND1A0556	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0557	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A0557	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0558	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A0558	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A0558	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0558	R1621054	PYTHON PROGRAMMING	F	0
17ND1A0558	R1621056	COMPUTER GRAPHICS	F	0
17ND1A0563	R1621053	DIGITAL LOGIC DESIGN	C	3
17ND1A0564	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A0564	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A0564	R1621056	COMPUTER GRAPHICS	F	0
17ND1A0565	R1621051	STATISTICS WITH R PROGRAMMING	B	3
17ND1A0565	R1621053	DIGITAL LOGIC DESIGN	C	3
17ND1A0567	R1621053	DIGITAL LOGIC DESIGN	F	0

Htno	Subcode	Subname	Grade	Credits
17ND1A0568	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0569	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0570	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
17ND1A0570	R1621054	PYTHON PROGRAMMING	F	0
17ND1A0570	R1621056	COMPUTER GRAPHICS	F	0
17ND1A0574	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A0574	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A0574	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0575	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
17ND1A0575	R1621055	DATA STRUCTURES THROUGH C++	F	0
17ND1A0576	R1621055	DATA STRUCTURES THROUGH C++	B	3
17ND1A0577	R1621051	STATISTICS WITH R PROGRAMMING	C	3
17ND1A0577	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
17ND1A0578	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A0578	R1621054	PYTHON PROGRAMMING	C	3
17ND1A0580	R1621054	PYTHON PROGRAMMING	C	3
17ND1A0590	R1621054	PYTHON PROGRAMMING	F	0
17ND1A0593	R1621051	STATISTICS WITH R PROGRAMMING	D	3
17ND1A0593	R1621056	COMPUTER GRAPHICS	D	3
17ND1A0596	R1621055	DATA STRUCTURES THROUGH C++	D	3
17ND1A05A4	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A05A4	R1621056	COMPUTER GRAPHICS	B	3
17ND1A05A6	R1621053	DIGITAL LOGIC DESIGN	B	3
17ND1A05B0	R1621051	STATISTICS WITH R PROGRAMMING	C	3
17ND1A05B0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A05B0	R1621053	DIGITAL LOGIC DESIGN	D	3
17ND1A05B5	R1621051	STATISTICS WITH R PROGRAMMING	D	3
17ND1A05B5	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A05B5	R1621054	PYTHON PROGRAMMING	F	0
17ND1A05B9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
17ND1A05C0	R1621055	DATA STRUCTURES THROUGH C++	C	3
17ND1A05C1	R1621054	PYTHON PROGRAMMING	F	0
17ND1A05C1	R1621056	COMPUTER GRAPHICS	F	0
17ND1A05C2	R1621051	STATISTICS WITH R PROGRAMMING	D	3
17ND1A05C2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A05C3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A05C3	R1621054	PYTHON PROGRAMMING	B	3
17ND1A05C4	R1621054	PYTHON PROGRAMMING	F	0
17ND1A05D0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A05D0	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A05D0	R1621054	PYTHON PROGRAMMING	F	0
17ND1A05D1	R1621054	PYTHON PROGRAMMING	D	3
17ND1A05D4	R1621051	STATISTICS WITH R PROGRAMMING	D	3
17ND1A05D4	R1621053	DIGITAL LOGIC DESIGN	D	3
17ND1A05D5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
17ND1A05D5	R1621053	DIGITAL LOGIC DESIGN	D	3
17ND1A05D5	R1621054	PYTHON PROGRAMMING	F	0
17ND1A05E2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A05E2	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A05E2	R1621056	COMPUTER GRAPHICS	F	0
17ND1A05E3	R1621051	STATISTICS WITH R PROGRAMMING	D	3

Htno	Subcode	Subname	Grade	Credits
17ND1A05E3	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A05E5	R1621054	PYTHON PROGRAMMING	F	0
17ND1A05E5	R1621055	DATA STRUCTURES THROUGH C++	F	0
17ND1A05E7	R1621051	STATISTICS WITH R PROGRAMMING	D	3
17ND1A05E7	R1621053	DIGITAL LOGIC DESIGN	D	3
17ND1A05F1	R1621051	STATISTICS WITH R PROGRAMMING	C	3
17ND1A05F1	R1621053	DIGITAL LOGIC DESIGN	D	3
17ND1A05F2	R1621056	COMPUTER GRAPHICS	D	3
17ND1A05F9	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17ND1A05F9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A05F9	R1621053	DIGITAL LOGIC DESIGN	F	0
17ND1A05G0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17ND1A05G2	R1621051	STATISTICS WITH R PROGRAMMING	D	3
17ND1A05G2	R1621055	DATA STRUCTURES THROUGH C++	D	3
17ND1A05G2	R1621056	COMPUTER GRAPHICS	F	0
17ND5A0403	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17ND5A0408	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17ND5A0408	R1621043	SIGNALS AND SYSTEMS	C	3
17ND5A0411	R1621043	SIGNALS AND SYSTEMS	F	0
17ND5A0411	R1621044	NETWORK ANALYSIS	C	3
17ND5A0411	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0401	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0401	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0401	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0401	R1621043	SIGNALS AND SYSTEMS	O	3
18ND1A0401	R1621044	NETWORK ANALYSIS	C	3
18ND1A0401	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0401	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0401	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0402	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0402	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0402	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0402	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0402	R1621044	NETWORK ANALYSIS	S	3
18ND1A0402	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0402	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0402	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0403	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0403	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0403	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0403	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0403	R1621044	NETWORK ANALYSIS	C	3
18ND1A0403	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0403	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0403	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0404	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A0404	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0404	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0404	R1621043	SIGNALS AND SYSTEMS	S	3
18ND1A0404	R1621044	NETWORK ANALYSIS	A	3
18ND1A0404	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	S	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0404	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0404	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0405	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0405	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0405	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0405	R1621043	SIGNALS AND SYSTEMS	S	3
18ND1A0405	R1621044	NETWORK ANALYSIS	C	3
18ND1A0405	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0405	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0405	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0406	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0406	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0406	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0406	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0406	R1621044	NETWORK ANALYSIS	A	3
18ND1A0406	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0406	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0406	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0407	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A0407	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0407	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0407	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0407	R1621044	NETWORK ANALYSIS	B	3
18ND1A0407	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0407	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0407	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0408	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0408	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0408	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0408	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0408	R1621044	NETWORK ANALYSIS	A	3
18ND1A0408	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0408	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0408	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0409	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0409	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0409	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0409	R1621043	SIGNALS AND SYSTEMS	O	3
18ND1A0409	R1621044	NETWORK ANALYSIS	B	3
18ND1A0409	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0409	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0409	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0410	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0410	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0410	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0410	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0410	R1621044	NETWORK ANALYSIS	B	3
18ND1A0410	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0410	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0410	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0411	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0411	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0411	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0411	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0411	R1621044	NETWORK ANALYSIS	F	0
18ND1A0411	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0411	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0411	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0413	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0413	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0413	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0413	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0413	R1621044	NETWORK ANALYSIS	A	3
18ND1A0413	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0413	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0413	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0414	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0414	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0414	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0414	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0414	R1621044	NETWORK ANALYSIS	D	3
18ND1A0414	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0414	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0414	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0415	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A0415	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0415	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0415	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0415	R1621044	NETWORK ANALYSIS	F	0
18ND1A0415	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0415	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	F	0
18ND1A0415	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A0416	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0416	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0416	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0416	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0416	R1621044	NETWORK ANALYSIS	B	3
18ND1A0416	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0416	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0416	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0417	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A0417	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0417	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0417	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0417	R1621044	NETWORK ANALYSIS	F	0
18ND1A0417	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0417	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	F	0
18ND1A0417	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A0418	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	S	3
18ND1A0418	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0418	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0418	R1621043	SIGNALS AND SYSTEMS	A	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0418	R1621044	NETWORK ANALYSIS	F	0
18ND1A0418	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0418	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0418	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0419	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0419	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0419	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0419	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0419	R1621044	NETWORK ANALYSIS	B	3
18ND1A0419	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0419	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0419	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0420	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0420	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0420	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0420	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0420	R1621044	NETWORK ANALYSIS	A	3
18ND1A0420	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	S	3
18ND1A0420	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0420	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0421	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A0421	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0421	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0421	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0421	R1621044	NETWORK ANALYSIS	A	3
18ND1A0421	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0421	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0421	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0422	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0422	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0422	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0422	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0422	R1621044	NETWORK ANALYSIS	F	0
18ND1A0422	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0422	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0422	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0423	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0423	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0423	R1621042	SWITCHING THEORY AND LOGIC DESIGN	O	3
18ND1A0423	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0423	R1621044	NETWORK ANALYSIS	S	3
18ND1A0423	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A0423	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0423	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0424	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0424	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A0424	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0424	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0424	R1621044	NETWORK ANALYSIS	S	3
18ND1A0424	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A0424	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A0424	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0425	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0425	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0425	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A0425	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0425	R1621044	NETWORK ANALYSIS	A	3
18ND1A0425	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0425	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0425	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0426	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0426	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0426	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0426	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0426	R1621044	NETWORK ANALYSIS	F	0
18ND1A0426	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0426	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0426	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0427	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0427	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0427	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A0427	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0427	R1621044	NETWORK ANALYSIS	F	0
18ND1A0427	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0427	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0427	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0428	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0428	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0428	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0428	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0428	R1621044	NETWORK ANALYSIS	S	3
18ND1A0428	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A0428	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0428	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0429	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0429	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0429	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0429	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0429	R1621044	NETWORK ANALYSIS	S	3
18ND1A0429	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0429	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0429	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0430	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A0430	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0430	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0430	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0430	R1621044	NETWORK ANALYSIS	F	0
18ND1A0430	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0430	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0430	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A0431	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0431	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0431	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0431	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0431	R1621044	NETWORK ANALYSIS	D	3
18ND1A0431	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0431	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0431	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0432	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0432	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0432	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0432	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0432	R1621044	NETWORK ANALYSIS	A	3
18ND1A0432	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0432	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0432	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0433	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0433	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0433	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0433	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0433	R1621044	NETWORK ANALYSIS	B	3
18ND1A0433	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0433	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0433	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0434	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A0434	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0434	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0434	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0434	R1621044	NETWORK ANALYSIS	F	0
18ND1A0434	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0434	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	F	0
18ND1A0434	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A0435	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0435	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0435	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0435	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0435	R1621044	NETWORK ANALYSIS	F	0
18ND1A0435	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0435	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0435	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0436	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0436	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A0436	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0436	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0436	R1621044	NETWORK ANALYSIS	C	3
18ND1A0436	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0436	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0436	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0437	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0437	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0437	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0437	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0437	R1621044	NETWORK ANALYSIS	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0437	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0437	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0437	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0438	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0438	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0438	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0438	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0438	R1621044	NETWORK ANALYSIS	F	0
18ND1A0438	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0438	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0438	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0439	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0439	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0439	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0439	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0439	R1621044	NETWORK ANALYSIS	C	3
18ND1A0439	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0439	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0439	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0440	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0440	R1621041	ELECTRONIC DEVICES AND CIRCUITS	O	3
18ND1A0440	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0440	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0440	R1621044	NETWORK ANALYSIS	S	3
18ND1A0440	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	S	3
18ND1A0440	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0440	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0441	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A0441	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0441	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0441	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0441	R1621044	NETWORK ANALYSIS	A	3
18ND1A0441	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0441	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0441	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0442	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A0442	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0442	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0442	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0442	R1621044	NETWORK ANALYSIS	B	3
18ND1A0442	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0442	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0442	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0443	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0443	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0443	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0443	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0443	R1621044	NETWORK ANALYSIS	S	3
18ND1A0443	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0443	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0443	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A0444	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0444	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A0444	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0444	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0444	R1621044	NETWORK ANALYSIS	A	3
18ND1A0444	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	S	3
18ND1A0444	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0444	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0445	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0445	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0445	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0445	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0445	R1621044	NETWORK ANALYSIS	A	3
18ND1A0445	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0445	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0445	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0446	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0446	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0446	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0446	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0446	R1621044	NETWORK ANALYSIS	D	3
18ND1A0446	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0446	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0446	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0447	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0447	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0447	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0447	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0447	R1621044	NETWORK ANALYSIS	D	3
18ND1A0447	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0447	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0447	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0448	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0448	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A0448	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0448	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0448	R1621044	NETWORK ANALYSIS	A	3
18ND1A0448	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0448	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0448	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0449	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0449	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A0449	R1621042	SWITCHING THEORY AND LOGIC DESIGN	S	3
18ND1A0449	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0449	R1621044	NETWORK ANALYSIS	S	3
18ND1A0449	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A0449	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0449	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0450	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0450	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0450	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0450	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0450	R1621044	NETWORK ANALYSIS	S	3
18ND1A0450	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0450	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0450	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0451	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0451	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0451	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0451	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0451	R1621044	NETWORK ANALYSIS	B	3
18ND1A0451	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0451	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0451	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0452	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0452	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A0452	R1621042	SWITCHING THEORY AND LOGIC DESIGN	S	3
18ND1A0452	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0452	R1621044	NETWORK ANALYSIS	A	3
18ND1A0452	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0452	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0452	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0453	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A0453	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0453	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0453	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0453	R1621044	NETWORK ANALYSIS	F	0
18ND1A0453	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0453	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	F	0
18ND1A0453	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A0454	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0454	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0454	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A0454	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0454	R1621044	NETWORK ANALYSIS	A	3
18ND1A0454	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0454	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0454	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0455	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0455	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0455	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0455	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0455	R1621044	NETWORK ANALYSIS	A	3
18ND1A0455	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0455	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0455	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0456	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0456	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A0456	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0456	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0456	R1621044	NETWORK ANALYSIS	B	3
18ND1A0456	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0456	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0456	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0457	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0457	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0457	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0457	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0457	R1621044	NETWORK ANALYSIS	A	3
18ND1A0457	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0457	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0457	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0458	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0458	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0458	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0458	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0458	R1621044	NETWORK ANALYSIS	F	0
18ND1A0458	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0458	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0458	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0459	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0459	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0459	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0459	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0459	R1621044	NETWORK ANALYSIS	A	3
18ND1A0459	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0459	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0459	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0460	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0460	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0460	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A0460	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0460	R1621044	NETWORK ANALYSIS	D	3
18ND1A0460	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0460	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0460	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	A	2
18ND1A0462	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0462	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0462	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0462	R1621043	SIGNALS AND SYSTEMS	S	3
18ND1A0462	R1621044	NETWORK ANALYSIS	B	3
18ND1A0462	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0462	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0462	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0463	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0463	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0463	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0463	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0463	R1621044	NETWORK ANALYSIS	A	3
18ND1A0463	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0463	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0463	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0464	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0464	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0464	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0464	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0464	R1621044	NETWORK ANALYSIS	C	3
18ND1A0464	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0464	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0464	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0465	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0465	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0465	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0465	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0465	R1621044	NETWORK ANALYSIS	A	3
18ND1A0465	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0465	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0465	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0466	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0466	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0466	R1621042	SWITCHING THEORY AND LOGIC DESIGN	S	3
18ND1A0466	R1621043	SIGNALS AND SYSTEMS	S	3
18ND1A0466	R1621044	NETWORK ANALYSIS	A	3
18ND1A0466	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0466	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0466	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0467	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0467	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0467	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0467	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0467	R1621044	NETWORK ANALYSIS	A	3
18ND1A0467	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0467	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0467	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0468	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0468	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0468	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0468	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0468	R1621044	NETWORK ANALYSIS	C	3
18ND1A0468	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0468	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0468	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0469	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0469	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0469	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0469	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0469	R1621044	NETWORK ANALYSIS	A	3
18ND1A0469	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0469	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0469	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0470	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0470	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0470	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0470	R1621043	SIGNALS AND SYSTEMS	D	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0470	R1621044	NETWORK ANALYSIS	F	0
18ND1A0470	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0470	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0470	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0471	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0471	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0471	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0471	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0471	R1621044	NETWORK ANALYSIS	A	3
18ND1A0471	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0471	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0471	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0472	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0472	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A0472	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0472	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0472	R1621044	NETWORK ANALYSIS	A	3
18ND1A0472	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0472	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0472	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0473	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A0473	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0473	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A0473	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0473	R1621044	NETWORK ANALYSIS	D	3
18ND1A0473	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0473	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0473	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0474	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0474	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0474	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0474	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0474	R1621044	NETWORK ANALYSIS	F	0
18ND1A0474	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0474	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0474	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0475	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0475	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0475	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0475	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0475	R1621044	NETWORK ANALYSIS	D	3
18ND1A0475	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0475	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0475	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0476	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0476	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0476	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0476	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0476	R1621044	NETWORK ANALYSIS	C	3
18ND1A0476	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0476	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A0476	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0477	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0477	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0477	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0477	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0477	R1621044	NETWORK ANALYSIS	S	3
18ND1A0477	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0477	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0477	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0478	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0478	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0478	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0478	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0478	R1621044	NETWORK ANALYSIS	F	0
18ND1A0478	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0478	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0478	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0479	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0479	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0479	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0479	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A0479	R1621044	NETWORK ANALYSIS	C	3
18ND1A0479	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0479	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0479	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0480	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0480	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0480	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A0480	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0480	R1621044	NETWORK ANALYSIS	B	3
18ND1A0480	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0480	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0480	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0481	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0481	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0481	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0481	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0481	R1621044	NETWORK ANALYSIS	C	3
18ND1A0481	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0481	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0481	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0482	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0482	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0482	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0482	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0482	R1621044	NETWORK ANALYSIS	D	3
18ND1A0482	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0482	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0482	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0483	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0483	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A0483	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0483	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0483	R1621044	NETWORK ANALYSIS	A	3
18ND1A0483	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0483	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0483	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0484	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0484	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0484	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0484	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0484	R1621044	NETWORK ANALYSIS	F	0
18ND1A0484	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0484	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
18ND1A0484	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0485	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A0485	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0485	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0485	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0485	R1621044	NETWORK ANALYSIS	A	3
18ND1A0485	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0485	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0485	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0486	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0486	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0486	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0486	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0486	R1621044	NETWORK ANALYSIS	C	3
18ND1A0486	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0486	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0486	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0487	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0487	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0487	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0487	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A0487	R1621044	NETWORK ANALYSIS	B	3
18ND1A0487	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A0487	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0487	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0488	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0488	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0488	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0488	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0488	R1621044	NETWORK ANALYSIS	B	3
18ND1A0488	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0488	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0488	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0489	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0489	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0489	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A0489	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0489	R1621044	NETWORK ANALYSIS	A	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0489	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0489	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0489	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0490	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0490	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0490	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0490	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0490	R1621044	NETWORK ANALYSIS	D	3
18ND1A0490	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A0490	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0490	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0491	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0491	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0491	R1621042	SWITCHING THEORY AND LOGIC DESIGN	S	3
18ND1A0491	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0491	R1621044	NETWORK ANALYSIS	B	3
18ND1A0491	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A0491	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0491	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0492	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0492	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0492	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0492	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0492	R1621044	NETWORK ANALYSIS	D	3
18ND1A0492	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0492	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0492	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0493	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0493	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A0493	R1621042	SWITCHING THEORY AND LOGIC DESIGN	S	3
18ND1A0493	R1621043	SIGNALS AND SYSTEMS	S	3
18ND1A0493	R1621044	NETWORK ANALYSIS	O	3
18ND1A0493	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0493	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0493	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0494	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A0494	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A0494	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0494	R1621043	SIGNALS AND SYSTEMS	O	3
18ND1A0494	R1621044	NETWORK ANALYSIS	C	3
18ND1A0494	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0494	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0494	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0495	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A0495	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0495	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A0495	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0495	R1621044	NETWORK ANALYSIS	B	3
18ND1A0495	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	S	3
18ND1A0495	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0495	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A0496	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A0496	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A0496	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0496	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A0496	R1621044	NETWORK ANALYSIS	F	0
18ND1A0496	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0496	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0496	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A0497	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A0497	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A0497	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0497	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A0497	R1621044	NETWORK ANALYSIS	S	3
18ND1A0497	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A0497	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0497	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0498	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A0498	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A0498	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A0498	R1621043	SIGNALS AND SYSTEMS	O	3
18ND1A0498	R1621044	NETWORK ANALYSIS	C	3
18ND1A0498	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A0498	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A0498	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0499	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A0499	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A0499	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A0499	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A0499	R1621044	NETWORK ANALYSIS	F	0
18ND1A0499	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A0499	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	F	0
18ND1A0499	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A04A0	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A04A0	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04A0	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04A0	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04A0	R1621044	NETWORK ANALYSIS	C	3
18ND1A04A0	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04A0	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A0	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04A1	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04A1	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04A1	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A04A1	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04A1	R1621044	NETWORK ANALYSIS	F	0
18ND1A04A1	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04A1	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A1	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04A2	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A04A2	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04A2	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3

Htno	Subcode	Subname	Grade	Credits
18ND1A04A2	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A04A2	R1621044	NETWORK ANALYSIS	F	0
18ND1A04A2	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04A2	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A2	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04A3	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04A3	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04A3	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04A3	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04A3	R1621044	NETWORK ANALYSIS	D	3
18ND1A04A3	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04A3	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A3	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04A4	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04A4	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04A4	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04A4	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04A4	R1621044	NETWORK ANALYSIS	C	3
18ND1A04A4	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04A4	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A4	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	A	2
18ND1A04A5	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A04A5	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04A5	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04A5	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04A5	R1621044	NETWORK ANALYSIS	S	3
18ND1A04A5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04A5	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A5	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04A6	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04A6	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04A6	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04A6	R1621043	SIGNALS AND SYSTEMS	S	3
18ND1A04A6	R1621044	NETWORK ANALYSIS	C	3
18ND1A04A6	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A04A6	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A6	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04A7	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A04A7	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04A7	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04A7	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04A7	R1621044	NETWORK ANALYSIS	D	3
18ND1A04A7	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04A7	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A7	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04A8	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04A8	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04A8	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04A8	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04A8	R1621044	NETWORK ANALYSIS	C	3
18ND1A04A8	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A04A8	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A8	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04A9	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04A9	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04A9	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04A9	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04A9	R1621044	NETWORK ANALYSIS	D	3
18ND1A04A9	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04A9	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04A9	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04B0	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04B0	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04B0	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04B0	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04B0	R1621044	NETWORK ANALYSIS	D	3
18ND1A04B0	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04B0	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
18ND1A04B0	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04B1	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04B1	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04B1	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04B1	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04B1	R1621044	NETWORK ANALYSIS	D	3
18ND1A04B1	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04B1	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
18ND1A04B1	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04B2	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04B2	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04B2	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04B2	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04B2	R1621044	NETWORK ANALYSIS	C	3
18ND1A04B2	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A04B2	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04B2	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04B3	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
18ND1A04B3	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04B3	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04B3	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04B3	R1621044	NETWORK ANALYSIS	B	3
18ND1A04B3	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04B3	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04B3	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04B4	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04B4	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04B4	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04B4	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A04B4	R1621044	NETWORK ANALYSIS	D	3
18ND1A04B4	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04B4	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04B4	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04B5	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A04B5	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04B5	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04B5	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04B5	R1621044	NETWORK ANALYSIS	F	0
18ND1A04B5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04B5	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	F	0
18ND1A04B5	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A04B6	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A04B6	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04B6	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04B6	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04B6	R1621044	NETWORK ANALYSIS	F	0
18ND1A04B6	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04B6	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	F	0
18ND1A04B6	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A04B7	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04B7	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04B7	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04B7	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04B7	R1621044	NETWORK ANALYSIS	O	3
18ND1A04B7	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A04B7	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04B7	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04B8	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A04B8	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04B8	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A04B8	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A04B8	R1621044	NETWORK ANALYSIS	C	3
18ND1A04B8	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04B8	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04B8	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04B9	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04B9	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04B9	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04B9	R1621043	SIGNALS AND SYSTEMS	S	3
18ND1A04B9	R1621044	NETWORK ANALYSIS	B	3
18ND1A04B9	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A04B9	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04B9	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04C0	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04C0	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04C0	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04C0	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04C0	R1621044	NETWORK ANALYSIS	B	3
18ND1A04C0	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04C0	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04C0	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04C1	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04C1	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04C1	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A04C1	R1621043	SIGNALS AND SYSTEMS	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A04C1	R1621044	NETWORK ANALYSIS	F	0
18ND1A04C1	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04C1	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04C1	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04C2	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04C2	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04C2	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A04C2	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04C2	R1621044	NETWORK ANALYSIS	D	3
18ND1A04C2	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04C2	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04C2	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04C3	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04C3	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04C3	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04C3	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A04C3	R1621044	NETWORK ANALYSIS	B	3
18ND1A04C3	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A04C3	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04C3	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04C4	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04C4	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A04C4	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A04C4	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04C4	R1621044	NETWORK ANALYSIS	A	3
18ND1A04C4	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04C4	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04C4	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04C5	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04C5	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04C5	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A04C5	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04C5	R1621044	NETWORK ANALYSIS	A	3
18ND1A04C5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A04C5	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04C5	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04C6	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04C6	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04C6	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04C6	R1621043	SIGNALS AND SYSTEMS	O	3
18ND1A04C6	R1621044	NETWORK ANALYSIS	B	3
18ND1A04C6	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	S	3
18ND1A04C6	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04C6	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04C7	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04C7	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04C7	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04C7	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04C7	R1621044	NETWORK ANALYSIS	B	3
18ND1A04C7	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04C7	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A04C7	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04C8	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04C8	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04C8	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04C8	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04C8	R1621044	NETWORK ANALYSIS	F	0
18ND1A04C8	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04C8	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04C8	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04C9	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04C9	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04C9	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04C9	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04C9	R1621044	NETWORK ANALYSIS	D	3
18ND1A04C9	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04C9	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04C9	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04D0	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04D0	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04D0	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04D0	R1621043	SIGNALS AND SYSTEMS	S	3
18ND1A04D0	R1621044	NETWORK ANALYSIS	C	3
18ND1A04D0	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04D0	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04D0	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04D1	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A04D1	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04D1	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A04D1	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04D1	R1621044	NETWORK ANALYSIS	F	0
18ND1A04D1	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04D1	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04D1	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04D2	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04D2	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04D2	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04D2	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04D2	R1621044	NETWORK ANALYSIS	C	3
18ND1A04D2	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04D2	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04D2	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	A	2
18ND1A04D3	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04D3	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04D3	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A04D3	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04D3	R1621044	NETWORK ANALYSIS	C	3
18ND1A04D3	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04D3	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04D3	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04D4	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04D4	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3

Htno	Subcode	Subname	Grade	Credits
18ND1A04D4	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04D4	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04D4	R1621044	NETWORK ANALYSIS	F	0
18ND1A04D4	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04D4	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04D4	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04D5	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04D5	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04D5	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A04D5	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04D5	R1621044	NETWORK ANALYSIS	F	0
18ND1A04D5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04D5	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	A	2
18ND1A04D5	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	A	2
18ND1A04D6	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A04D6	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04D6	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A04D6	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04D6	R1621044	NETWORK ANALYSIS	D	3
18ND1A04D6	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04D6	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04D6	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04D7	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04D7	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04D7	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04D7	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04D7	R1621044	NETWORK ANALYSIS	C	3
18ND1A04D7	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04D7	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04D7	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04D8	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04D8	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04D8	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04D8	R1621043	SIGNALS AND SYSTEMS	O	3
18ND1A04D8	R1621044	NETWORK ANALYSIS	B	3
18ND1A04D8	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A04D8	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04D8	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04D9	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04D9	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04D9	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04D9	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A04D9	R1621044	NETWORK ANALYSIS	S	3
18ND1A04D9	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04D9	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04D9	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04E0	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04E0	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A04E0	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A04E0	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04E0	R1621044	NETWORK ANALYSIS	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A04E0	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04E0	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04E0	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04E1	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04E1	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04E1	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04E1	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04E1	R1621044	NETWORK ANALYSIS	C	3
18ND1A04E1	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04E1	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04E1	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04E2	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04E2	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04E2	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04E2	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04E2	R1621044	NETWORK ANALYSIS	F	0
18ND1A04E2	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04E2	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
18ND1A04E2	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04E3	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04E3	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04E3	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04E3	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04E3	R1621044	NETWORK ANALYSIS	B	3
18ND1A04E3	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04E3	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04E3	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04E4	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04E4	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A04E4	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04E4	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04E4	R1621044	NETWORK ANALYSIS	A	3
18ND1A04E4	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04E4	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04E4	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04E5	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A04E5	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04E5	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04E5	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04E5	R1621044	NETWORK ANALYSIS	F	0
18ND1A04E5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04E5	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	F	0
18ND1A04E5	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A04E6	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04E6	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04E6	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04E6	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04E6	R1621044	NETWORK ANALYSIS	F	0
18ND1A04E6	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04E6	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04E6	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2

Htno	Subcode	Subname	Grade	Credits
18ND1A04E7	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04E7	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04E7	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04E7	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04E7	R1621044	NETWORK ANALYSIS	B	3
18ND1A04E7	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A04E7	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04E7	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04E8	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04E8	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
18ND1A04E8	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04E8	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04E8	R1621044	NETWORK ANALYSIS	C	3
18ND1A04E8	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04E8	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04E8	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04E9	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04E9	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04E9	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A04E9	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04E9	R1621044	NETWORK ANALYSIS	S	3
18ND1A04E9	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A04E9	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04E9	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04F0	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04F0	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04F0	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04F0	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04F0	R1621044	NETWORK ANALYSIS	D	3
18ND1A04F0	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04F0	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04F0	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04F1	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04F1	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04F1	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04F1	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04F1	R1621044	NETWORK ANALYSIS	B	3
18ND1A04F1	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04F1	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
18ND1A04F1	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04F2	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18ND1A04F2	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04F2	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04F2	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04F2	R1621044	NETWORK ANALYSIS	F	0
18ND1A04F2	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04F2	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
18ND1A04F2	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04F3	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04F3	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04F3	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3

Htno	Subcode	Subname	Grade	Credits
18ND1A04F3	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04F3	R1621044	NETWORK ANALYSIS	B	3
18ND1A04F3	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04F3	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04F3	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04F4	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04F4	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04F4	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04F4	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04F4	R1621044	NETWORK ANALYSIS	F	0
18ND1A04F4	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04F4	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04F4	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04F5	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04F5	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04F5	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04F5	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04F5	R1621044	NETWORK ANALYSIS	A	3
18ND1A04F5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04F5	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04F5	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04F6	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04F6	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04F6	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04F6	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04F6	R1621044	NETWORK ANALYSIS	F	0
18ND1A04F6	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04F6	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04F6	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04F7	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04F7	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04F7	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04F7	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04F7	R1621044	NETWORK ANALYSIS	C	3
18ND1A04F7	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04F7	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04F7	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04F8	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04F8	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04F8	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04F8	R1621043	SIGNALS AND SYSTEMS	A	3
18ND1A04F8	R1621044	NETWORK ANALYSIS	D	3
18ND1A04F8	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04F8	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04F8	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04F9	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04F9	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04F9	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04F9	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04F9	R1621044	NETWORK ANALYSIS	B	3
18ND1A04F9	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A04F9	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04F9	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04G0	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04G0	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
18ND1A04G0	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04G0	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04G0	R1621044	NETWORK ANALYSIS	F	0
18ND1A04G0	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	A	3
18ND1A04G0	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04G0	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04G1	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18ND1A04G1	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04G1	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A04G1	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04G1	R1621044	NETWORK ANALYSIS	D	3
18ND1A04G1	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04G1	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04G1	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04G2	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04G2	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
18ND1A04G2	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18ND1A04G2	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04G2	R1621044	NETWORK ANALYSIS	F	0
18ND1A04G2	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04G2	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04G2	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04G3	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04G3	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04G3	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18ND1A04G3	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04G3	R1621044	NETWORK ANALYSIS	D	3
18ND1A04G3	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04G3	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04G3	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04G4	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04G4	R1621041	ELECTRONIC DEVICES AND CIRCUITS	O	3
18ND1A04G4	R1621042	SWITCHING THEORY AND LOGIC DESIGN	S	3
18ND1A04G4	R1621043	SIGNALS AND SYSTEMS	C	3
18ND1A04G4	R1621044	NETWORK ANALYSIS	C	3
18ND1A04G4	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18ND1A04G4	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04G4	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04G5	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04G5	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04G5	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04G5	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04G5	R1621044	NETWORK ANALYSIS	A	3
18ND1A04G5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18ND1A04G5	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04G5	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04G6	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A04G6	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18ND1A04G6	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18ND1A04G6	R1621043	SIGNALS AND SYSTEMS	F	0
18ND1A04G6	R1621044	NETWORK ANALYSIS	F	0
18ND1A04G6	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND1A04G6	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	F	0
18ND1A04G6	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
18ND1A04G7	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04G7	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
18ND1A04G7	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A04G7	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04G7	R1621044	NETWORK ANALYSIS	C	3
18ND1A04G7	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04G7	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04G7	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
18ND1A04G8	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
18ND1A04G8	R1621041	ELECTRONIC DEVICES AND CIRCUITS	O	3
18ND1A04G8	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
18ND1A04G8	R1621043	SIGNALS AND SYSTEMS	B	3
18ND1A04G8	R1621044	NETWORK ANALYSIS	B	3
18ND1A04G8	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	O	3
18ND1A04G8	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04G8	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A04G9	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
18ND1A04G9	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
18ND1A04G9	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND1A04G9	R1621043	SIGNALS AND SYSTEMS	D	3
18ND1A04G9	R1621044	NETWORK ANALYSIS	A	3
18ND1A04G9	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND1A04G9	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
18ND1A04G9	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
18ND1A0501	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A0501	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0501	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0501	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0501	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0501	R1621056	COMPUTER GRAPHICS	S	3
18ND1A0501	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0501	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0502	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0502	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0502	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0502	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0502	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0502	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0502	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0502	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0503	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0503	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0503	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0503	R1621054	PYTHON PROGRAMMING	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A0503	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0503	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0503	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0503	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0504	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0504	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0504	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0504	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0504	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0504	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0504	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0504	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0505	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0505	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0505	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0505	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0505	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0505	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0505	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0505	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0506	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0506	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A0506	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0506	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0506	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0506	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0506	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0506	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0507	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0507	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0507	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0507	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0507	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0507	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0507	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0507	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0508	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0508	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0508	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0508	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0508	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0508	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0508	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0508	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0509	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A0509	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0509	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0509	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0509	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0509	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0509	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A0509	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0510	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0510	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0510	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0510	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0510	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0510	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0510	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0510	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0511	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0511	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0511	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0511	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0511	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0511	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0511	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0511	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0512	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0512	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0512	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0512	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0512	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0512	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0512	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0512	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0513	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A0513	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0513	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0513	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0513	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0513	R1621056	COMPUTER GRAPHICS	A	3
18ND1A0513	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0513	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0514	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0514	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0514	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0514	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0514	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0514	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0514	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0514	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0515	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0515	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A0515	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0515	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0515	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0515	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0515	R1621057	DATA STRUCTURES THROUGH C++ LAB	A	2
18ND1A0515	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A0516	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0516	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0516	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0516	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0516	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A0516	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0516	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0516	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0517	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0517	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0517	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0517	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0517	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0517	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0517	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0517	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A0518	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0518	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0518	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0518	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0518	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A0518	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0518	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0518	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0519	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0519	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0519	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0519	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0519	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0519	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0519	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0519	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0520	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0520	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0520	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0520	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0520	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0520	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0520	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0520	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0521	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0521	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0521	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0521	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0521	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0521	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0521	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0521	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0522	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0522	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A0522	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0522	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0522	R1621055	DATA STRUCTURES THROUGH C++	B	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0522	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0522	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0522	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0523	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0523	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0523	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0523	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0523	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0523	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0523	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0523	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0524	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0524	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0524	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0524	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0524	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0524	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0524	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0524	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0525	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0525	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0525	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0525	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0525	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0525	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0525	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0525	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0526	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0526	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0526	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0526	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0526	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0526	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0526	R1621057	DATA STRUCTURES THROUGH C++ LAB	A	2
18ND1A0526	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A0527	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A0527	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0527	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0527	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0527	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0527	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0527	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0527	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0528	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0528	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0528	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0528	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0528	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0528	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0528	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0528	R1621058	PYTHON PROGRAMMING LAB	S	2

Htno	Subcode	Subname	Grade	Credits
18ND1A0529	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0529	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0529	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0529	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0529	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0529	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0529	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0529	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0530	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0530	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0530	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0530	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0530	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0530	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0530	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0530	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A0531	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0531	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0531	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0531	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0531	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0531	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0531	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0531	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A0532	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0532	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0532	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0532	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0532	R1621055	DATA STRUCTURES THROUGH C++	S	3
18ND1A0532	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0532	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0532	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0533	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0533	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0533	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0533	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0533	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0533	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0533	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0533	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0534	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0534	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0534	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0534	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0534	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0534	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0534	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0534	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0535	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0535	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0535	R1621053	DIGITAL LOGIC DESIGN	A	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0535	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0535	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0535	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0535	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0535	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0536	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A0536	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0536	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0536	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0536	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0536	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0536	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0536	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0537	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0537	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0537	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0537	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0537	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0537	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0537	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0537	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0538	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0538	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A0538	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0538	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0538	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0538	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0538	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0538	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0539	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0539	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0539	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0539	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0539	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0539	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0539	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0539	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0540	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0540	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0540	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0540	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0540	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0540	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0540	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0540	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A0541	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0541	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0541	R1621053	DIGITAL LOGIC DESIGN	S	3
18ND1A0541	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0541	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0541	R1621056	COMPUTER GRAPHICS	B	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0541	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0541	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0542	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0542	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0542	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0542	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0542	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0542	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0542	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0542	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0543	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0543	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0543	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0543	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0543	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0543	R1621056	COMPUTER GRAPHICS	A	3
18ND1A0543	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0543	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0544	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0544	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0544	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0544	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0544	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0544	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0544	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0544	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0545	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0545	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A0545	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0545	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0545	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0545	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0545	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0545	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0546	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0546	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0546	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0546	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0546	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0546	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0546	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0546	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0547	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0547	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0547	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0547	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0547	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0547	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0547	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A0547	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A0548	R1621051	STATISTICS WITH R PROGRAMMING	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0548	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A0548	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0548	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0548	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0548	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0548	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0548	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0549	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0549	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0549	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0549	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0549	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0549	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0549	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0549	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0550	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0550	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0550	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0550	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0550	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0550	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0550	R1621057	DATA STRUCTURES THROUGH C++ LAB	A	2
18ND1A0550	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0551	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0551	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0551	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0551	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0551	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0551	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0551	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0551	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0552	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0552	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0552	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0552	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0552	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0552	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0552	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0552	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0553	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0553	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0553	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0553	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0553	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0553	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0553	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0553	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0555	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0555	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0555	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0555	R1621054	PYTHON PROGRAMMING	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A0555	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0555	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0555	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A0555	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A0556	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0556	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0556	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0556	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0556	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0556	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0556	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0556	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0557	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0557	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0557	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0557	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0557	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0557	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0557	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0557	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0558	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0558	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A0558	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0558	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0558	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0558	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0558	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0558	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0559	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0559	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0559	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0559	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0559	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0559	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0559	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0559	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0560	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0560	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0560	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0560	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0560	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0560	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0560	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0560	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0561	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0561	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0561	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0561	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0561	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0561	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0561	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2

Htno	Subcode	Subname	Grade	Credits
18ND1A0561	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A0562	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0562	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0562	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0562	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0562	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0562	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0562	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0562	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0563	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0563	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0563	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0563	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0563	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A0563	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0563	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0563	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0564	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0564	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0564	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0564	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0564	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0564	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0564	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0564	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0565	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A0565	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0565	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0565	R1621054	PYTHON PROGRAMMING	A	3
18ND1A0565	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0565	R1621056	COMPUTER GRAPHICS	A	3
18ND1A0565	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0565	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0566	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0566	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A0566	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0566	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0566	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0566	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0566	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0566	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0567	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0567	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0567	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0567	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0567	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0567	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0567	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0567	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0568	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0568	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0568	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0568	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0568	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0568	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0568	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0568	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0569	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A0569	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0569	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0569	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0569	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0569	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0569	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0569	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0570	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0570	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0570	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0570	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0570	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0570	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0570	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0570	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A0571	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0571	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0571	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0571	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0571	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0571	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0571	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0571	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0573	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0573	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0573	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0573	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0573	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0573	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0573	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0573	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0574	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0574	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0574	R1621053	DIGITAL LOGIC DESIGN	S	3
18ND1A0574	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0574	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0574	R1621056	COMPUTER GRAPHICS	A	3
18ND1A0574	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0574	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0575	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0575	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0575	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0575	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0575	R1621055	DATA STRUCTURES THROUGH C++	B	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0575	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0575	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0575	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0576	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0576	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0576	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0576	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0576	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0576	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0576	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0576	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0577	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0577	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0577	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0577	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0577	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A0577	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0577	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0577	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0578	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0578	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0578	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0578	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0578	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0578	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0578	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0578	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0579	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0579	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0579	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0579	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0579	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0579	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0579	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0579	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0580	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0580	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0580	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0580	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0580	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0580	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0580	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0580	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0581	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0581	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0581	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0581	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0581	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0581	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0581	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0581	R1621058	PYTHON PROGRAMMING LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A0582	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0582	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3
18ND1A0582	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0582	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0582	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0582	R1621056	COMPUTER GRAPHICS	A	3
18ND1A0582	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0582	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0583	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0583	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0583	R1621053	DIGITAL LOGIC DESIGN	S	3
18ND1A0583	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0583	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A0583	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0583	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0583	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0584	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0584	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3
18ND1A0584	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0584	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0584	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0584	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0584	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0584	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0585	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0585	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0585	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0585	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0585	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0585	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0585	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0585	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A0586	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0586	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0586	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A0586	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0586	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0586	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0586	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0586	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0587	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0587	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0587	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0587	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0587	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0587	R1621056	COMPUTER GRAPHICS	B	3
18ND1A0587	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0587	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0588	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0588	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0588	R1621053	DIGITAL LOGIC DESIGN	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0588	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0588	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0588	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0588	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0588	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0589	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A0589	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0589	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0589	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0589	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A0589	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0589	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0589	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0590	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0590	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0590	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0590	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0590	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0590	R1621056	COMPUTER GRAPHICS	A	3
18ND1A0590	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0590	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0591	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0591	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A0591	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0591	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0591	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A0591	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0591	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0591	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0592	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0592	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A0592	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A0592	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0592	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0592	R1621056	COMPUTER GRAPHICS	F	0
18ND1A0592	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0592	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0593	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0593	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0593	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0593	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0593	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0593	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0593	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0593	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0594	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0594	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0594	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0594	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0594	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0594	R1621056	COMPUTER GRAPHICS	B	3

Htno	Subcode	Subname	Grade	Credits
18ND1A0594	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0594	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0595	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A0595	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A0595	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0595	R1621054	PYTHON PROGRAMMING	C	3
18ND1A0595	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0595	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0595	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0595	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0596	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A0596	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3
18ND1A0596	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A0596	R1621054	PYTHON PROGRAMMING	B	3
18ND1A0596	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A0596	R1621056	COMPUTER GRAPHICS	D	3
18ND1A0596	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A0596	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0597	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0597	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A0597	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A0597	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0597	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A0597	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0597	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0597	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0598	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A0598	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0598	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0598	R1621054	PYTHON PROGRAMMING	F	0
18ND1A0598	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A0598	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0598	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0598	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A0599	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A0599	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A0599	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A0599	R1621054	PYTHON PROGRAMMING	D	3
18ND1A0599	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A0599	R1621056	COMPUTER GRAPHICS	C	3
18ND1A0599	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A0599	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A05A0	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05A0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05A0	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05A0	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05A0	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05A0	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05A0	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05A0	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05A1	R1621051	STATISTICS WITH R PROGRAMMING	B	3

Htno	Subcode	Subname	Grade	Credits
18ND1A05A1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05A1	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05A1	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05A1	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A05A1	R1621056	COMPUTER GRAPHICS	A	3
18ND1A05A1	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05A1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05A2	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05A2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05A2	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05A2	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05A2	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05A2	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05A2	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05A2	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05A3	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05A3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05A3	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05A3	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05A3	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05A3	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05A3	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A05A3	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A05A4	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05A4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05A4	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A05A4	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05A4	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05A4	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05A4	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05A4	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05A5	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05A5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05A5	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05A5	R1621054	PYTHON PROGRAMMING	A	3
18ND1A05A5	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05A5	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05A5	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05A5	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05A6	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05A6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05A6	R1621053	DIGITAL LOGIC DESIGN	S	3
18ND1A05A6	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05A6	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05A6	R1621056	COMPUTER GRAPHICS	A	3
18ND1A05A6	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05A6	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05A7	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05A7	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05A7	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05A7	R1621054	PYTHON PROGRAMMING	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A05A7	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05A7	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05A7	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A05A7	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A05A8	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05A8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05A8	R1621053	DIGITAL LOGIC DESIGN	S	3
18ND1A05A8	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05A8	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05A8	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05A8	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05A8	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05A9	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A05A9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05A9	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05A9	R1621054	PYTHON PROGRAMMING	A	3
18ND1A05A9	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05A9	R1621056	COMPUTER GRAPHICS	A	3
18ND1A05A9	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05A9	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05B0	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05B0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05B0	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05B0	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05B0	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05B0	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05B0	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05B0	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05B1	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05B1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05B1	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05B1	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05B1	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05B1	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05B1	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05B1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05B2	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05B2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05B2	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05B2	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05B2	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05B2	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05B2	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05B2	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05B3	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A05B3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05B3	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05B3	R1621054	PYTHON PROGRAMMING	A	3
18ND1A05B3	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05B3	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05B3	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A05B3	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05B4	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05B4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05B4	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05B4	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05B4	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05B4	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05B4	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05B4	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05B5	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05B5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05B5	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05B5	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05B5	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05B5	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05B5	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A05B5	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A05B6	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A05B6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05B6	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05B6	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05B6	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05B6	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05B6	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05B6	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05B7	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A05B7	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05B7	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05B7	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05B7	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05B7	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05B7	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05B7	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05B8	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05B8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05B8	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05B8	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05B8	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05B8	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05B8	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05B8	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05B9	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05B9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05B9	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05B9	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05B9	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05B9	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05B9	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05B9	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C0	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05C0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3

Htno	Subcode	Subname	Grade	Credits
18ND1A05C0	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A05C0	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05C0	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05C0	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05C0	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05C0	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C1	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05C1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05C1	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05C1	R1621054	PYTHON PROGRAMMING	A	3
18ND1A05C1	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05C1	R1621056	COMPUTER GRAPHICS	A	3
18ND1A05C1	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05C1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C2	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05C2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05C2	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05C2	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05C2	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05C2	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05C2	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05C2	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C3	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05C3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05C3	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05C3	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05C3	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05C3	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05C3	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05C3	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C4	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05C4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05C4	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05C4	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05C4	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05C4	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05C4	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05C4	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C5	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05C5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05C5	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05C5	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05C5	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05C5	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05C5	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05C5	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C6	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05C6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05C6	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05C6	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05C6	R1621055	DATA STRUCTURES THROUGH C++	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A05C6	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05C6	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05C6	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C7	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A05C7	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05C7	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05C7	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05C7	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05C7	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05C7	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05C7	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C8	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05C8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05C8	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05C8	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05C8	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05C8	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05C8	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05C8	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05C9	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05C9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05C9	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05C9	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05C9	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A05C9	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05C9	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05C9	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05D0	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05D0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05D0	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05D0	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05D0	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05D0	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05D0	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05D0	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05D1	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A05D1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3
18ND1A05D1	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05D1	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05D1	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A05D1	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05D1	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05D1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05D2	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A05D2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05D2	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A05D2	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05D2	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05D2	R1621056	COMPUTER GRAPHICS	S	3
18ND1A05D2	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05D2	R1621058	PYTHON PROGRAMMING LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A05D3	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05D3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05D3	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05D3	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05D3	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05D3	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05D3	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05D3	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05D4	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05D4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05D4	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05D4	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05D4	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05D4	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05D4	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05D4	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05D5	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05D5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05D5	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05D5	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05D5	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05D5	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05D5	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A05D5	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A05D6	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05D6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05D6	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05D6	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05D6	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05D6	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05D6	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05D6	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05D7	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05D7	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05D7	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05D7	R1621054	PYTHON PROGRAMMING	B	3
18ND1A05D7	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05D7	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05D7	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05D7	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05D8	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05D8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05D8	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05D8	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05D8	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05D8	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05D8	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A05D8	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A05D9	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05D9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3
18ND1A05D9	R1621053	DIGITAL LOGIC DESIGN	B	3

Htno	Subcode	Subname	Grade	Credits
18ND1A05D9	R1621054	PYTHON PROGRAMMING	B	3
18ND1A05D9	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A05D9	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05D9	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05D9	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05E0	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05E0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05E0	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05E0	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05E0	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05E0	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05E0	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05E0	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05E1	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05E1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05E1	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05E1	R1621054	PYTHON PROGRAMMING	B	3
18ND1A05E1	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05E1	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05E1	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05E1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05E2	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05E2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05E2	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05E2	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05E2	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05E2	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05E2	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05E2	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05E3	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05E3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05E3	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05E3	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05E3	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05E3	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05E3	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A05E3	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A05E4	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05E4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05E4	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05E4	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05E4	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05E4	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05E4	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05E4	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05E5	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05E5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05E5	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A05E5	R1621054	PYTHON PROGRAMMING	A	3
18ND1A05E5	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05E5	R1621056	COMPUTER GRAPHICS	A	3

Htno	Subcode	Subname	Grade	Credits
18ND1A05E5	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05E5	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05E6	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05E6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05E6	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05E6	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05E6	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05E6	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05E6	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05E6	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05E7	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05E7	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3
18ND1A05E7	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05E7	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05E7	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05E7	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05E7	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05E7	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05E8	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05E8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05E8	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A05E8	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05E8	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05E8	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05E8	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05E8	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05E9	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05E9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05E9	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05E9	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05E9	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05E9	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05E9	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05E9	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A05F0	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05F0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05F0	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05F0	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05F0	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05F0	R1621056	COMPUTER GRAPHICS	A	3
18ND1A05F0	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05F0	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05F1	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05F1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05F1	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05F1	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05F1	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05F1	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05F1	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05F1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05F2	R1621051	STATISTICS WITH R PROGRAMMING	C	3

Htno	Subcode	Subname	Grade	Credits
18ND1A05F2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05F2	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05F2	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05F2	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05F2	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05F2	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05F2	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05F3	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05F3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05F3	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05F3	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05F3	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05F3	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05F3	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05F3	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05F4	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05F4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05F4	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05F4	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05F4	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05F4	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05F4	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A05F4	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A05F5	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05F5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05F5	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05F5	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05F5	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05F5	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05F5	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05F5	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05F6	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05F6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05F6	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05F6	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05F6	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05F6	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05F6	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05F6	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05F7	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05F7	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05F7	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05F7	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05F7	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05F7	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05F7	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05F7	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05F8	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05F8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05F8	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05F8	R1621054	PYTHON PROGRAMMING	F	0

Htno	Subcode	Subname	Grade	Credits
18ND1A05F8	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05F8	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05F8	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
18ND1A05F8	R1621058	PYTHON PROGRAMMING LAB	F	0
18ND1A05F9	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05F9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05F9	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05F9	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05F9	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05F9	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05F9	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05F9	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G0	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05G0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05G0	R1621053	DIGITAL LOGIC DESIGN	S	3
18ND1A05G0	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05G0	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05G0	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05G0	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05G0	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G1	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05G1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05G1	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05G1	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05G1	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05G1	R1621056	COMPUTER GRAPHICS	A	3
18ND1A05G1	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05G1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G2	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05G2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05G2	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05G2	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05G2	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05G2	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05G2	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05G2	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G3	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05G3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05G3	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05G3	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05G3	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05G3	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05G3	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05G3	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G4	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05G4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05G4	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05G4	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05G4	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05G4	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05G4	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2

Htno	Subcode	Subname	Grade	Credits
18ND1A05G4	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G5	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05G5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05G5	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05G5	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05G5	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05G5	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05G5	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05G5	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G6	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05G6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05G6	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05G6	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05G6	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05G6	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05G6	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05G6	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G7	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05G7	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05G7	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05G7	R1621054	PYTHON PROGRAMMING	B	3
18ND1A05G7	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05G7	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05G7	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05G7	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G8	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05G8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05G8	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05G8	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05G8	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05G8	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05G8	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05G8	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05G9	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05G9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05G9	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05G9	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05G9	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05G9	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05G9	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05G9	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A05H0	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A05H0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05H0	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05H0	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05H0	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05H0	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05H0	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05H0	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05H1	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05H1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3

Htno	Subcode	Subname	Grade	Credits
18ND1A05H1	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05H1	R1621054	PYTHON PROGRAMMING	B	3
18ND1A05H1	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05H1	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05H1	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05H1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05H2	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05H2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05H2	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05H2	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05H2	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05H2	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05H2	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05H2	R1621058	PYTHON PROGRAMMING LAB	A	2
18ND1A05H3	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05H3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05H3	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05H3	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05H3	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05H3	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05H3	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05H3	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05H4	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05H4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05H4	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05H4	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05H4	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05H4	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05H4	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05H4	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05H5	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05H5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3
18ND1A05H5	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05H5	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05H5	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A05H5	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05H5	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05H5	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05H6	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05H6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05H6	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05H6	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05H6	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05H6	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05H6	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05H6	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05H7	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05H7	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05H7	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05H7	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05H7	R1621055	DATA STRUCTURES THROUGH C++	B	3

Htno	Subcode	Subname	Grade	Credits
18ND1A05H7	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05H7	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05H7	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05H8	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05H8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
18ND1A05H8	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05H8	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05H8	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05H8	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05H8	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05H8	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05H9	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05H9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05H9	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05H9	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05H9	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05H9	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05H9	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05H9	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05I0	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05I0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05I0	R1621053	DIGITAL LOGIC DESIGN	D	3
18ND1A05I0	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05I0	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05I0	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05I0	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05I0	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A05I1	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05I1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05I1	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05I1	R1621054	PYTHON PROGRAMMING	B	3
18ND1A05I1	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05I1	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05I1	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05I1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05I2	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05I2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05I2	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05I2	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05I2	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05I2	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05I2	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05I2	R1621058	PYTHON PROGRAMMING LAB	S	2
18ND1A05I3	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05I3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3
18ND1A05I3	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A05I3	R1621054	PYTHON PROGRAMMING	A	3
18ND1A05I3	R1621055	DATA STRUCTURES THROUGH C++	A	3
18ND1A05I3	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05I3	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05I3	R1621058	PYTHON PROGRAMMING LAB	O	2

Htno	Subcode	Subname	Grade	Credits
18ND1A05I4	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05I4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05I4	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05I4	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05I4	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05I4	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05I4	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05I4	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05I5	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05I5	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05I5	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05I5	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05I5	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05I5	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05I5	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05I5	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05I6	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05I6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05I6	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05I6	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05I6	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05I6	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05I6	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05I6	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05I7	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05I7	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18ND1A05I7	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05I7	R1621054	PYTHON PROGRAMMING	B	3
18ND1A05I7	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05I7	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05I7	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05I7	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05I8	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05I8	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	A	3
18ND1A05I8	R1621053	DIGITAL LOGIC DESIGN	A	3
18ND1A05I8	R1621054	PYTHON PROGRAMMING	D	3
18ND1A05I8	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05I8	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05I8	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05I8	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05I9	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05I9	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05I9	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05I9	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05I9	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05I9	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05I9	R1621057	DATA STRUCTURES THROUGH C++ LAB	S	2
18ND1A05I9	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05J0	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18ND1A05J0	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05J0	R1621053	DIGITAL LOGIC DESIGN	D	3

Htno	Subcode	Subname	Grade	Credits
18ND1A05J0	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05J0	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05J0	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05J0	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05J0	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05J1	R1621051	STATISTICS WITH R PROGRAMMING	A	3
18ND1A05J1	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	S	3
18ND1A05J1	R1621053	DIGITAL LOGIC DESIGN	B	3
18ND1A05J1	R1621054	PYTHON PROGRAMMING	B	3
18ND1A05J1	R1621055	DATA STRUCTURES THROUGH C++	C	3
18ND1A05J1	R1621056	COMPUTER GRAPHICS	C	3
18ND1A05J1	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05J1	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05J2	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18ND1A05J2	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05J2	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05J2	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05J2	R1621055	DATA STRUCTURES THROUGH C++	D	3
18ND1A05J2	R1621056	COMPUTER GRAPHICS	D	3
18ND1A05J2	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05J2	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05J3	R1621051	STATISTICS WITH R PROGRAMMING	B	3
18ND1A05J3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18ND1A05J3	R1621053	DIGITAL LOGIC DESIGN	F	0
18ND1A05J3	R1621054	PYTHON PROGRAMMING	C	3
18ND1A05J3	R1621055	DATA STRUCTURES THROUGH C++	B	3
18ND1A05J3	R1621056	COMPUTER GRAPHICS	B	3
18ND1A05J3	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05J3	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND1A05J4	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18ND1A05J4	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
18ND1A05J4	R1621053	DIGITAL LOGIC DESIGN	C	3
18ND1A05J4	R1621054	PYTHON PROGRAMMING	F	0
18ND1A05J4	R1621055	DATA STRUCTURES THROUGH C++	F	0
18ND1A05J4	R1621056	COMPUTER GRAPHICS	F	0
18ND1A05J4	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
18ND1A05J4	R1621058	PYTHON PROGRAMMING LAB	O	2
18ND5A0406	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18ND5A0406	R1621044	NETWORK ANALYSIS	F	0
18ND5A0407	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18ND5A0416	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18ND5A0421	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0401	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
19ND5A0401	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
19ND5A0401	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
19ND5A0401	R1621043	SIGNALS AND SYSTEMS	S	3
19ND5A0401	R1621044	NETWORK ANALYSIS	A	3
19ND5A0401	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0401	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0401	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0402	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3

Htno	Subcode	Subname	Grade	Credits
19ND5A0402	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
19ND5A0402	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0402	R1621043	SIGNALS AND SYSTEMS	C	3
19ND5A0402	R1621044	NETWORK ANALYSIS	D	3
19ND5A0402	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0402	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0402	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0403	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19ND5A0403	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
19ND5A0403	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
19ND5A0403	R1621043	SIGNALS AND SYSTEMS	F	0
19ND5A0403	R1621044	NETWORK ANALYSIS	C	3
19ND5A0403	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0403	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0403	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0404	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0404	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
19ND5A0404	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0404	R1621043	SIGNALS AND SYSTEMS	C	3
19ND5A0404	R1621044	NETWORK ANALYSIS	A	3
19ND5A0404	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
19ND5A0404	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0404	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0405	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
19ND5A0405	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
19ND5A0405	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
19ND5A0405	R1621043	SIGNALS AND SYSTEMS	F	0
19ND5A0405	R1621044	NETWORK ANALYSIS	F	0
19ND5A0405	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0405	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
19ND5A0405	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
19ND5A0406	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0406	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
19ND5A0406	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
19ND5A0406	R1621043	SIGNALS AND SYSTEMS	B	3
19ND5A0406	R1621044	NETWORK ANALYSIS	D	3
19ND5A0406	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
19ND5A0406	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0406	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0407	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19ND5A0407	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
19ND5A0407	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
19ND5A0407	R1621043	SIGNALS AND SYSTEMS	D	3
19ND5A0407	R1621044	NETWORK ANALYSIS	F	0
19ND5A0407	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0407	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0407	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0408	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
19ND5A0408	R1621041	ELECTRONIC DEVICES AND CIRCUITS	S	3
19ND5A0408	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
19ND5A0408	R1621043	SIGNALS AND SYSTEMS	B	3

Htno	Subcode	Subname	Grade	Credits
19ND5A0408	R1621044	NETWORK ANALYSIS	A	3
19ND5A0408	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
19ND5A0408	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0408	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0409	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19ND5A0409	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
19ND5A0409	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
19ND5A0409	R1621043	SIGNALS AND SYSTEMS	D	3
19ND5A0409	R1621044	NETWORK ANALYSIS	F	0
19ND5A0409	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0409	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0409	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
19ND5A0410	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19ND5A0410	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
19ND5A0410	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
19ND5A0410	R1621043	SIGNALS AND SYSTEMS	C	3
19ND5A0410	R1621044	NETWORK ANALYSIS	C	3
19ND5A0410	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
19ND5A0410	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
19ND5A0410	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
19ND5A0411	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0411	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
19ND5A0411	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
19ND5A0411	R1621043	SIGNALS AND SYSTEMS	F	0
19ND5A0411	R1621044	NETWORK ANALYSIS	A	3
19ND5A0411	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0411	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0411	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0413	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0413	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
19ND5A0413	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
19ND5A0413	R1621043	SIGNALS AND SYSTEMS	B	3
19ND5A0413	R1621044	NETWORK ANALYSIS	B	3
19ND5A0413	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0413	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0413	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0414	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19ND5A0414	R1621041	ELECTRONIC DEVICES AND CIRCUITS	D	3
19ND5A0414	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
19ND5A0414	R1621043	SIGNALS AND SYSTEMS	B	3
19ND5A0414	R1621044	NETWORK ANALYSIS	B	3
19ND5A0414	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
19ND5A0414	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0414	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0415	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0415	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
19ND5A0415	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0415	R1621043	SIGNALS AND SYSTEMS	D	3
19ND5A0415	R1621044	NETWORK ANALYSIS	C	3
19ND5A0415	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
19ND5A0415	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2

Htno	Subcode	Subname	Grade	Credits
19ND5A0415	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
19ND5A0416	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0416	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
19ND5A0416	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
19ND5A0416	R1621043	SIGNALS AND SYSTEMS	B	3
19ND5A0416	R1621044	NETWORK ANALYSIS	A	3
19ND5A0416	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
19ND5A0416	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0416	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0417	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0417	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
19ND5A0417	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0417	R1621043	SIGNALS AND SYSTEMS	D	3
19ND5A0417	R1621044	NETWORK ANALYSIS	C	3
19ND5A0417	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
19ND5A0417	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0417	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0418	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
19ND5A0418	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
19ND5A0418	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0418	R1621043	SIGNALS AND SYSTEMS	F	0
19ND5A0418	R1621044	NETWORK ANALYSIS	D	3
19ND5A0418	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0418	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0418	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0419	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19ND5A0419	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
19ND5A0419	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0419	R1621043	SIGNALS AND SYSTEMS	F	0
19ND5A0419	R1621044	NETWORK ANALYSIS	B	3
19ND5A0419	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0419	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0419	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0420	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0420	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
19ND5A0420	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
19ND5A0420	R1621043	SIGNALS AND SYSTEMS	B	3
19ND5A0420	R1621044	NETWORK ANALYSIS	B	3
19ND5A0420	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
19ND5A0420	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0420	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0421	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
19ND5A0421	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
19ND5A0421	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
19ND5A0421	R1621043	SIGNALS AND SYSTEMS	S	3
19ND5A0421	R1621044	NETWORK ANALYSIS	S	3
19ND5A0421	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
19ND5A0421	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0421	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0422	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19ND5A0422	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0

Htno	Subcode	Subname	Grade	Credits
19ND5A0422	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0422	R1621043	SIGNALS AND SYSTEMS	D	3
19ND5A0422	R1621044	NETWORK ANALYSIS	D	3
19ND5A0422	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0422	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
19ND5A0422	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0423	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0423	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
19ND5A0423	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0423	R1621043	SIGNALS AND SYSTEMS	F	0
19ND5A0423	R1621044	NETWORK ANALYSIS	C	3
19ND5A0423	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0423	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0423	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	S	2
19ND5A0424	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
19ND5A0424	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
19ND5A0424	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
19ND5A0424	R1621043	SIGNALS AND SYSTEMS	D	3
19ND5A0424	R1621044	NETWORK ANALYSIS	B	3
19ND5A0424	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
19ND5A0424	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0424	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0425	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0425	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
19ND5A0425	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0425	R1621043	SIGNALS AND SYSTEMS	A	3
19ND5A0425	R1621044	NETWORK ANALYSIS	D	3
19ND5A0425	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
19ND5A0425	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0425	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0426	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19ND5A0426	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
19ND5A0426	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
19ND5A0426	R1621043	SIGNALS AND SYSTEMS	C	3
19ND5A0426	R1621044	NETWORK ANALYSIS	B	3
19ND5A0426	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
19ND5A0426	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0426	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0427	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19ND5A0427	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
19ND5A0427	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0427	R1621043	SIGNALS AND SYSTEMS	F	0
19ND5A0427	R1621044	NETWORK ANALYSIS	F	0
19ND5A0427	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19ND5A0427	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
19ND5A0427	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	F	0
19ND5A0428	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
19ND5A0428	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
19ND5A0428	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
19ND5A0428	R1621043	SIGNALS AND SYSTEMS	A	3
19ND5A0428	R1621044	NETWORK ANALYSIS	C	3

Htno	Subcode	Subname	Grade	Credits
19ND5A0428	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
19ND5A0428	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	S	2
19ND5A0428	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0429	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
19ND5A0429	R1621041	ELECTRONIC DEVICES AND CIRCUITS	C	3
19ND5A0429	R1621042	SWITCHING THEORY AND LOGIC DESIGN	A	3
19ND5A0429	R1621043	SIGNALS AND SYSTEMS	B	3
19ND5A0429	R1621044	NETWORK ANALYSIS	C	3
19ND5A0429	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
19ND5A0429	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0429	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0431	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	C	3
19ND5A0431	R1621041	ELECTRONIC DEVICES AND CIRCUITS	A	3
19ND5A0431	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0431	R1621043	SIGNALS AND SYSTEMS	F	0
19ND5A0431	R1621044	NETWORK ANALYSIS	A	3
19ND5A0431	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
19ND5A0431	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0431	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0432	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	B	3
19ND5A0432	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
19ND5A0432	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0432	R1621043	SIGNALS AND SYSTEMS	C	3
19ND5A0432	R1621044	NETWORK ANALYSIS	S	3
19ND5A0432	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
19ND5A0432	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0432	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0433	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	A	3
19ND5A0433	R1621041	ELECTRONIC DEVICES AND CIRCUITS	B	3
19ND5A0433	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19ND5A0433	R1621043	SIGNALS AND SYSTEMS	O	3
19ND5A0433	R1621044	NETWORK ANALYSIS	C	3
19ND5A0433	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
19ND5A0433	R1621046	ELECTRONIC DEVICES AND CIRCUITS LAB	O	2
19ND5A0433	R1621047	NETWORKS & ELECTRICAL TECHNOLOGY LAB	O	2
19ND5A0501	R1621051	STATISTICS WITH R PROGRAMMING	D	3
19ND5A0501	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
19ND5A0501	R1621053	DIGITAL LOGIC DESIGN	D	3
19ND5A0501	R1621054	PYTHON PROGRAMMING	B	3
19ND5A0501	R1621055	DATA STRUCTURES THROUGH C++	C	3
19ND5A0501	R1621056	COMPUTER GRAPHICS	C	3
19ND5A0501	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
19ND5A0501	R1621058	PYTHON PROGRAMMING LAB	O	2
19ND5A0502	R1621051	STATISTICS WITH R PROGRAMMING	C	3
19ND5A0502	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
19ND5A0502	R1621053	DIGITAL LOGIC DESIGN	D	3
19ND5A0502	R1621054	PYTHON PROGRAMMING	C	3
19ND5A0502	R1621055	DATA STRUCTURES THROUGH C++	D	3
19ND5A0502	R1621056	COMPUTER GRAPHICS	C	3
19ND5A0502	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
19ND5A0502	R1621058	PYTHON PROGRAMMING LAB	O	2

Htno	Subcode	Subname	Grade	Credits
19ND5A0503	R1621051	STATISTICS WITH R PROGRAMMING	B	3
19ND5A0503	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
19ND5A0503	R1621053	DIGITAL LOGIC DESIGN	B	3
19ND5A0503	R1621054	PYTHON PROGRAMMING	A	3
19ND5A0503	R1621055	DATA STRUCTURES THROUGH C++	B	3
19ND5A0503	R1621056	COMPUTER GRAPHICS	B	3
19ND5A0503	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
19ND5A0503	R1621058	PYTHON PROGRAMMING LAB	O	2
19ND5A0504	R1621051	STATISTICS WITH R PROGRAMMING	C	3
19ND5A0504	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
19ND5A0504	R1621053	DIGITAL LOGIC DESIGN	C	3
19ND5A0504	R1621054	PYTHON PROGRAMMING	D	3
19ND5A0504	R1621055	DATA STRUCTURES THROUGH C++	C	3
19ND5A0504	R1621056	COMPUTER GRAPHICS	F	0
19ND5A0504	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
19ND5A0504	R1621058	PYTHON PROGRAMMING LAB	O	2
19ND5A0505	R1621051	STATISTICS WITH R PROGRAMMING	F	0
19ND5A0505	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
19ND5A0505	R1621053	DIGITAL LOGIC DESIGN	F	0
19ND5A0505	R1621054	PYTHON PROGRAMMING	F	0
19ND5A0505	R1621055	DATA STRUCTURES THROUGH C++	F	0
19ND5A0505	R1621056	COMPUTER GRAPHICS	F	0
19ND5A0505	R1621057	DATA STRUCTURES THROUGH C++ LAB	F	0
19ND5A0505	R1621058	PYTHON PROGRAMMING LAB	F	0
19ND5A0506	R1621051	STATISTICS WITH R PROGRAMMING	B	3
19ND5A0506	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
19ND5A0506	R1621053	DIGITAL LOGIC DESIGN	B	3
19ND5A0506	R1621054	PYTHON PROGRAMMING	C	3
19ND5A0506	R1621055	DATA STRUCTURES THROUGH C++	F	0
19ND5A0506	R1621056	COMPUTER GRAPHICS	C	3
19ND5A0506	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
19ND5A0506	R1621058	PYTHON PROGRAMMING LAB	O	2
19ND5A0507	R1621051	STATISTICS WITH R PROGRAMMING	B	3
19ND5A0507	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
19ND5A0507	R1621053	DIGITAL LOGIC DESIGN	C	3
19ND5A0507	R1621054	PYTHON PROGRAMMING	B	3
19ND5A0507	R1621055	DATA STRUCTURES THROUGH C++	C	3
19ND5A0507	R1621056	COMPUTER GRAPHICS	C	3
19ND5A0507	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
19ND5A0507	R1621058	PYTHON PROGRAMMING LAB	O	2
19ND5A0508	R1621051	STATISTICS WITH R PROGRAMMING	C	3
19ND5A0508	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
19ND5A0508	R1621053	DIGITAL LOGIC DESIGN	F	0
19ND5A0508	R1621054	PYTHON PROGRAMMING	C	3
19ND5A0508	R1621055	DATA STRUCTURES THROUGH C++	F	0
19ND5A0508	R1621056	COMPUTER GRAPHICS	A	3
19ND5A0508	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
19ND5A0508	R1621058	PYTHON PROGRAMMING LAB	O	2
19ND5A0509	R1621051	STATISTICS WITH R PROGRAMMING	A	3
19ND5A0509	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	B	3
19ND5A0509	R1621053	DIGITAL LOGIC DESIGN	B	3

Htno	Subcode	Subname	Grade	Credits
19ND5A0509	R1621054	PYTHON PROGRAMMING	B	3
19ND5A0509	R1621055	DATA STRUCTURES THROUGH C++	C	3
19ND5A0509	R1621056	COMPUTER GRAPHICS	B	3
19ND5A0509	R1621057	DATA STRUCTURES THROUGH C++ LAB	O	2
19ND5A0509	R1621058	PYTHON PROGRAMMING LAB	O	2

**Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 10-01-2020]

** Note:**

* -1 in the filed of externals indicates student is absent for the respective subject.

* -2 in the filed of externals indicates student result Withheld for the respective subject.

* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.



Date:02.01.2020

Controller of Examinations